

**REMARKS/ARGUMENTS**

Claims 1-20 are pending in the application. Claims 3-9 and 14-20 are withdrawn from consideration. Claims 1, 2 and 10-13 are presented for examination. The claims have not been amended.

Claims 1, 2, and 10-13 are rejected under 35 USC §102(b) as being anticipated by Aoki (5,943,519).

Claim 1 recites in part “a finalizing process circuit for executing finalizing process for said recording medium.” The recited “finalizing” has a specific meaning in the optical disk recording arts. The recited finalize process is clearly discussed on page 25, line 18 to page 26, line 13. Aoki does not show the recited “finalizing circuit.” In fact, Aoki is not even related to the recording media of the present invention. Instead, Aoki is directed to a still video camera. Aoki therefore does not disclose the recited “a finalizing process circuit for executing finalizing process for said recording medium.”

Claim 1 further recites in part “when either one of the remaining capacity of said first electric power source and voltage of said second electric power source is detected to be equal or higher than a predetermined value, in said first detector circuit and said second detector circuit, operation of finalizing process is started.” Aoki disclose circuitry for detecting whether power to the still video camera comes from an internal source or an external source.

For example, the cited col. 7, lines 10-20 of Aoki clearly describe “image-erasing means” and “disabling means.” The “disabling means” disables erasing if power is not detected from the internal source or from the external source. The “image-erasing means” is for erasing the developed image from the electro-developing recording medium. Aoki’s “image-erasing means” is not the “finalizing” that is recited in the pending claims. Col. 7, lines 10-20 in the Aoki reference therefore does not teach “when either one of the remaining capacity of said first electric power source and voltage of said second electric power source is detected to be equal or higher than a predetermined value, in said first detector circuit and said second detector circuit, operation of finalizing process is started.”


Aoki at col. 22, lines 1-65 was also cited. However, a careful review of column 22 reveals a discussion of detection of the source of power, whether from an internal source or an external source. For example, the first three full paragraphs are a discussion about the main power source circuit 77 and a constituent power input detecting circuit 87. The discussion is about how the circuitry can detect the source of power (internal or external) when the device is switched on. The fourth paragraph describes the operation called auto-photographing/recording mode. The fifth paragraph describes manual-photographing mode. Neither the auto-photographing/recording mode nor the manual-photographing mode taught by Aoki corresponds to the recited "finalizing process." Col. 22, of the Aoki reference therefore does not teach "when either one of the remaining capacity of said first electric power source and voltage of said second electric power source is detected to be equal or higher than a predetermined value, in said first detector circuit and said second detector circuit, operation of finalizing process is started."

#### CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,

  
George B. F. Yee  
Reg. No. 37,478

TOWNSEND and TOWNSEND and CREW LLP  
Two Embarcadero Center, Eighth Floor  
San Francisco, California 94111-3834  
Tel: 650-326-2400  
Fax: 415-576-0300  
GBFY:gjs  
60765284 v1